

U.S. Patent Application Serial No. **09/417,705**
Response filed November 3, 2004
Reply to OA dated August 3, 2004

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-9 (Canceled):

1 Claim 10 (Currently Amended): A digital camera, comprising:

2 an imaging device for outputting, at a rate of one screen per a first period, a first image signal

3 of a first resolution which corresponds to an optical image of an object scene;

4 a creator for creating [[an]] a second image signal corresponding to an optical image of an
5 object scene every first period of a second resolution lower than the first resolution by subjecting the
6 first image signal outputted from said imaging device to a signal processing including a thinning-out
7 process;

8 a memory having a single input/output port and a plurality of memory areas;

9 a changer for changing a selecting of [[the]] a memory area at an interval of the first period;

10 a writer for writing the second image signal created by said creator to one of said plurality
11 of memory areas based on a changing result of said changer;

12 a reader for reading the second image signal from another of said plurality of memory areas

13 based on the changing result of said changer [[every]] at a rate of one screen per a second period
14 which is one [[for]] over an integer of the first period; and

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15 a display for displaying an image based on the second image signal read out by said reader,
16 wherein said memory is an SDRAM, and said writer includes a buffer for holding the second image
17 signal created by said creator, and a transferor for transferring the second image signal held by said
18 buffer to said memory.

Claims 11 and 12 (Canceled).

1 Claim 13 (Previously Presented): A digital camera according to claim 10, wherein the
2 number of the memory areas is two, and the second period is $\frac{1}{2}$ of the first period.

1 Claim 14 (Previously Presented): A digital camera according to claim 10, wherein said
2 changer changes the selecting of the memory area in a predetermined order.

3 Claim 15 (New): A digital camera, comprising:

4 an imaging device for outputting, at a rate of one screen per a first period, a first image signal
5 of a first resolution which corresponds to an optical image of an object scene;
6 a creator for creating a second image signal of a second resolution lower than the first
7 resolution by subjecting the first image signal outputted from said imaging device to a signal
8 processing including a thinning-out process;

9 a memory having a single input/output port;

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10 a writer for writing the second image signal created by said creator to said memory;
11 a reader for reading the second image signal from said memory at a rate of one screen per a
12 second period which is shorter than the first period; and
13 a display for displaying an image based on the second image signal read out by said reader,
14 wherein said memory is an SDRAM, and said writer includes a buffer for holding the second image
15 signal held by said buffer to said memory.

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